

**CLEAN SUBSTITUTE ABSTRACT**

The present invention is directed to a demodulating circuit adapted for reproducing, from an input signal which has been caused to undergo transmission through a desired transmission system, data sequential caused to undergo transmission through the input signal, which is adapted to perform phase comparison, by Exclusive-OR (EX-OR) circuits (254Q), (254I), between a first oscillating output signal generated by an oscillator (252) and a variable frequency divider (253) and a second oscillating output signal different from the first oscillating output signal by 90 degrees ( $\pi/2$ ) in phase to judge control direction by a control direction judgment circuit (257) on the basis of polarities of the phase comparison results to integrate the control direction judgment results by one period of the input signal by an integrating circuit (258) to perform correction processing having a predetermined controlled variable in the case where the phase difference is  $\pm \pi/2$  on the basis of the phase comparison result by a correction circuit (259) with respect to the integrated result to control the operation of the variable frequency divider (253) by the correction-processed control signal outputted once per each period.